

<b>Notice of References Cited</b>	Application/Control No. 09/650,604	Applicant(s)/Patent Under Reexamination KRUTSICK, THOMAS J.	
	Examiner A. Sefer	Art Unit 2826	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,046,491	04-2000	Tamagawa	257/538
	B	US-4,948,747	08-1990	Pfiester	437/60
	C	US-5,804,857	09-1998	Tsuji	257/346
	D	US-4,609,935	09-1986	Kondo	357/51
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 57-100723	06-1982	Japan	Mitsuyama et al	
	O	JP 1-130552	05-1998	Japan	Tanaka et al	
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	D. Jaume et al., "High-Voltage Planar Devices Using Field Plate and Semi-Resistive Layers" IEEE Transactions on Electron Devices, VOL. 38, No. 7, July 1991.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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